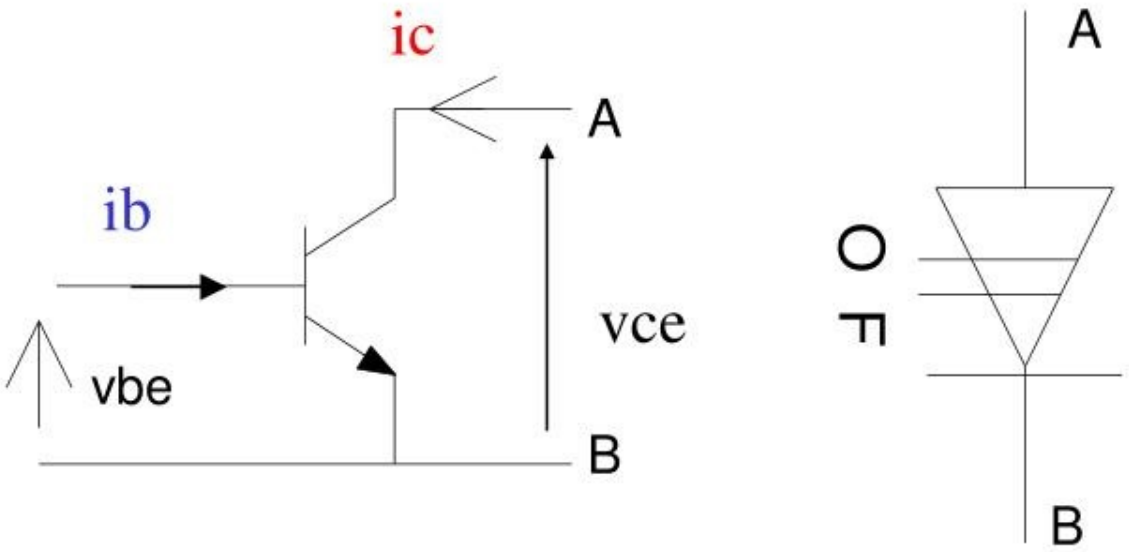
Hacheur

Support de cours

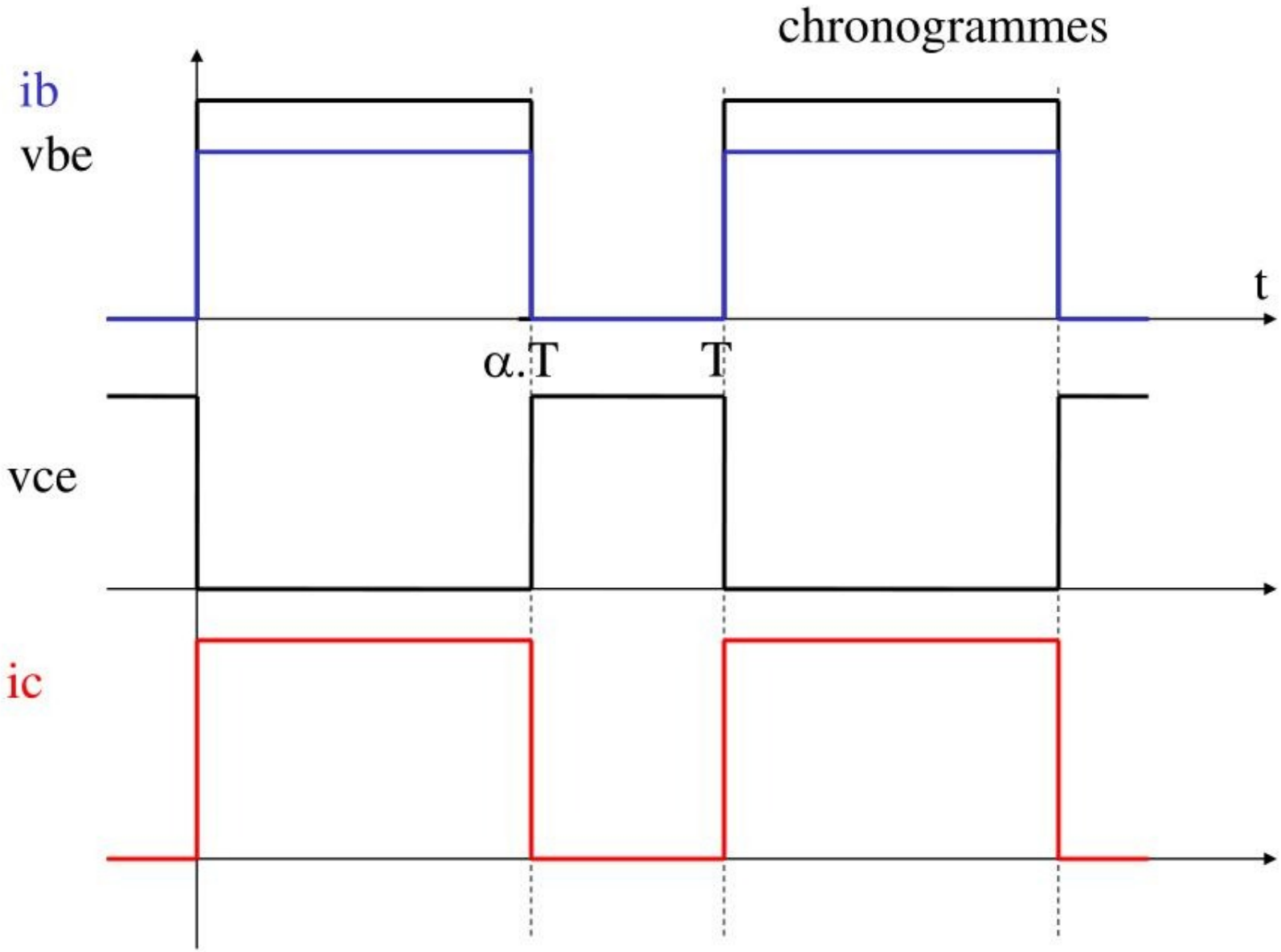
Christian PERRIN
christian.perrin@ac-nancy-metz.fr

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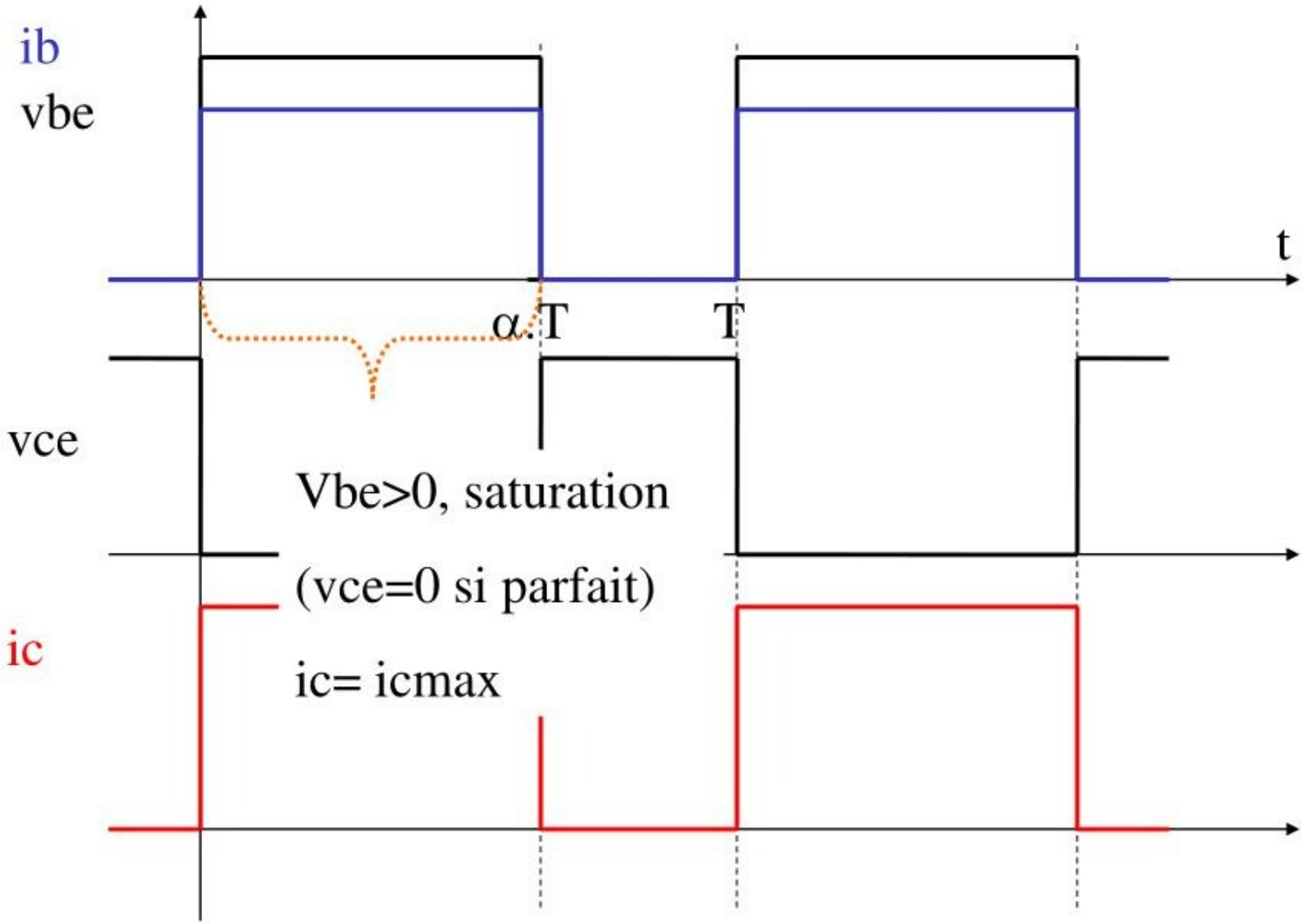
I)Rappel fonctionnement interrupteur unidirectionnel à transistor



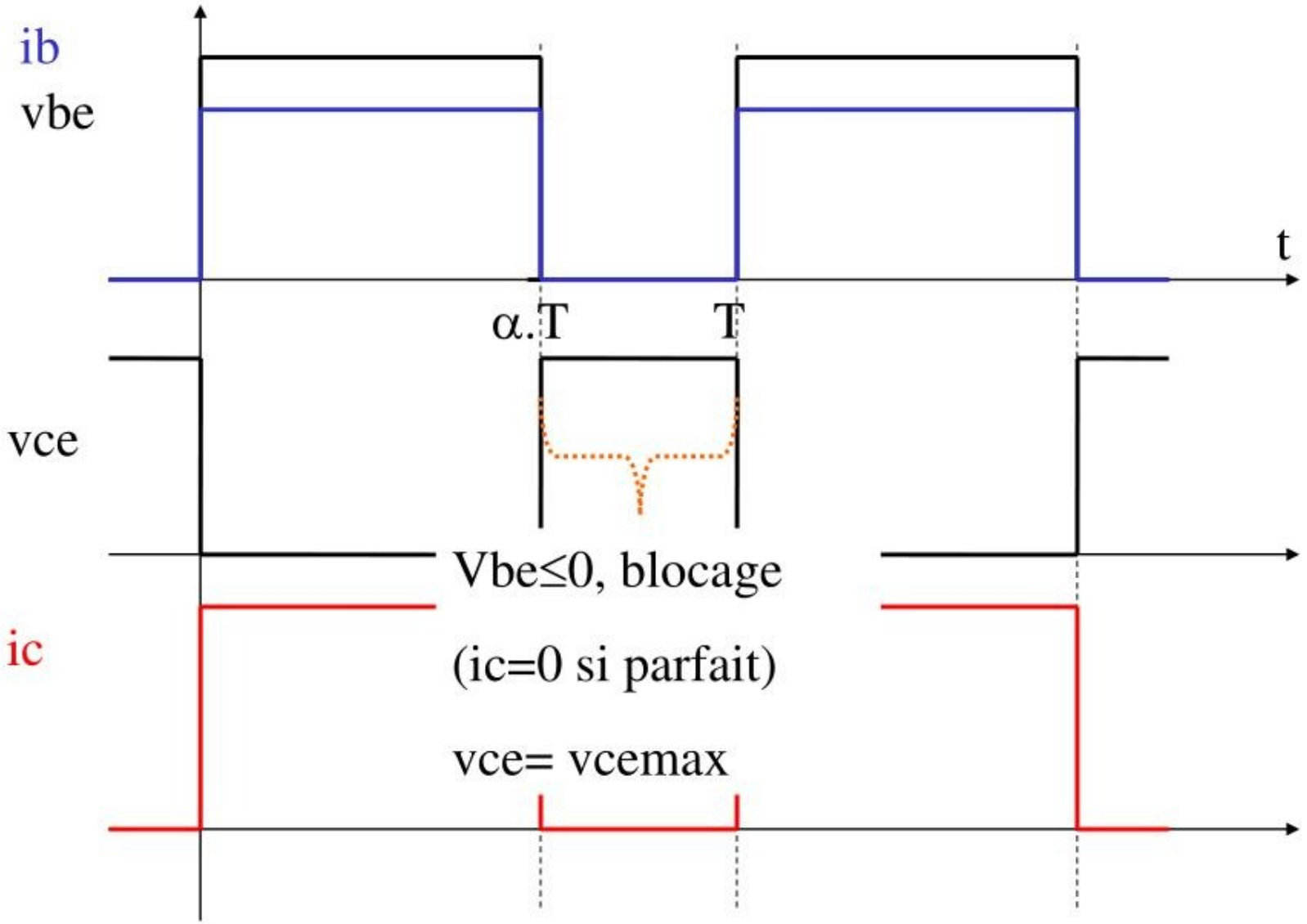
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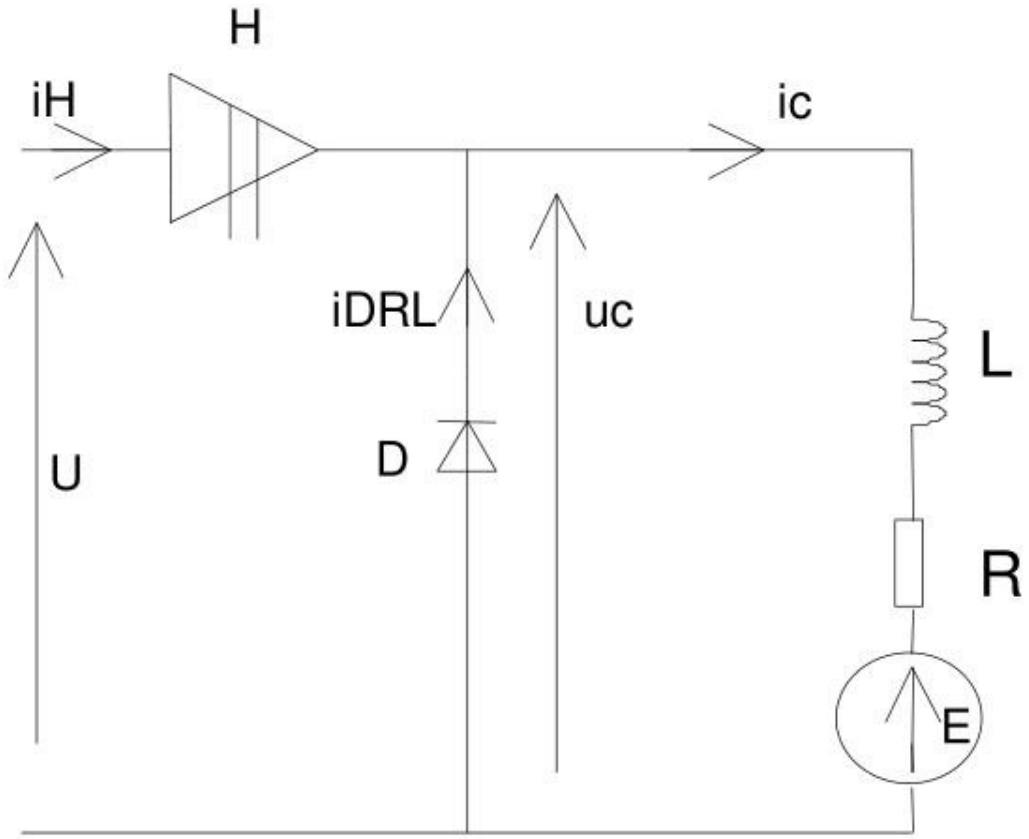


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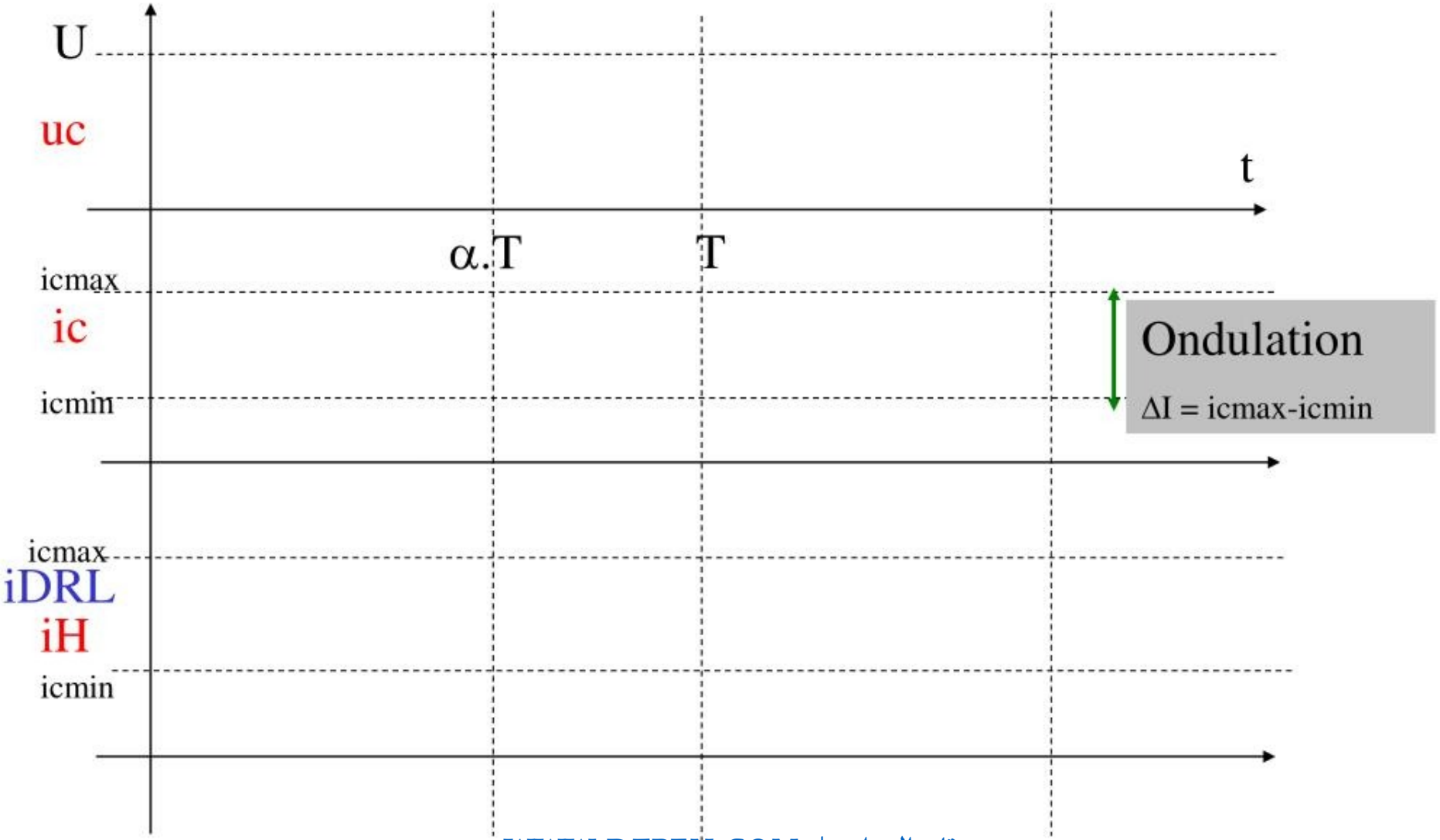
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II) Hacheur série



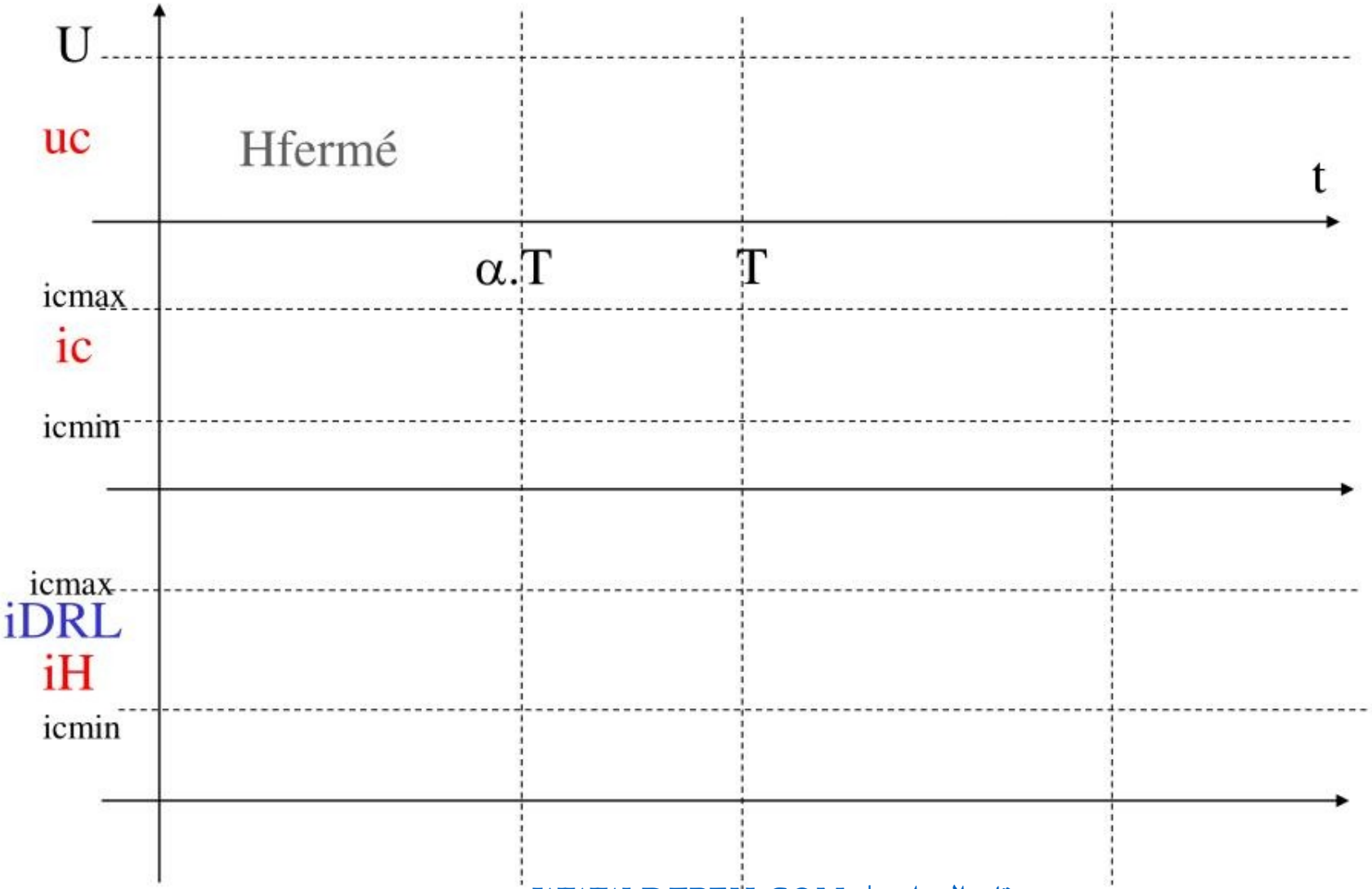
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1) Courant ininterrompu



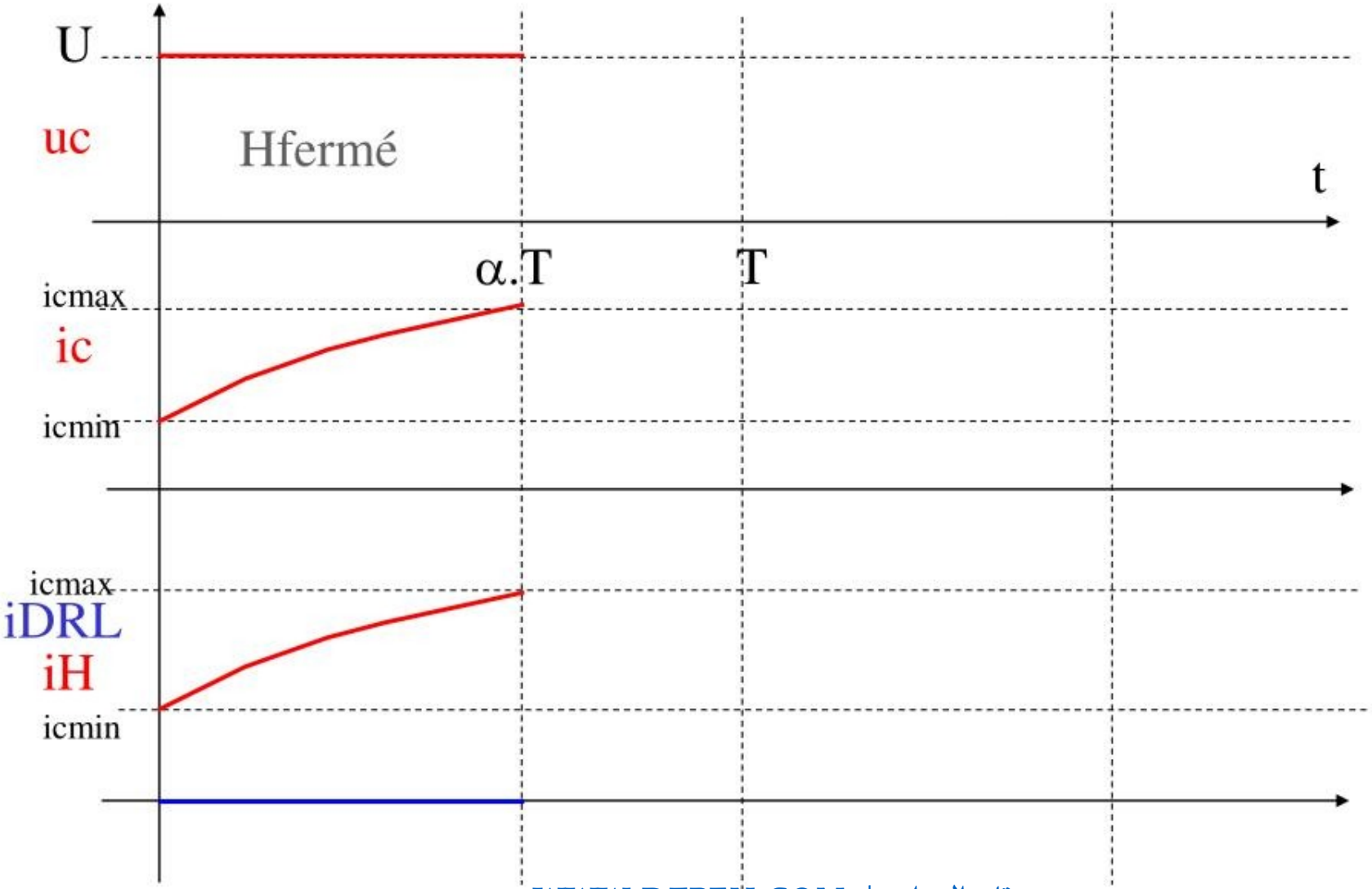
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1) Courant ininterrompu



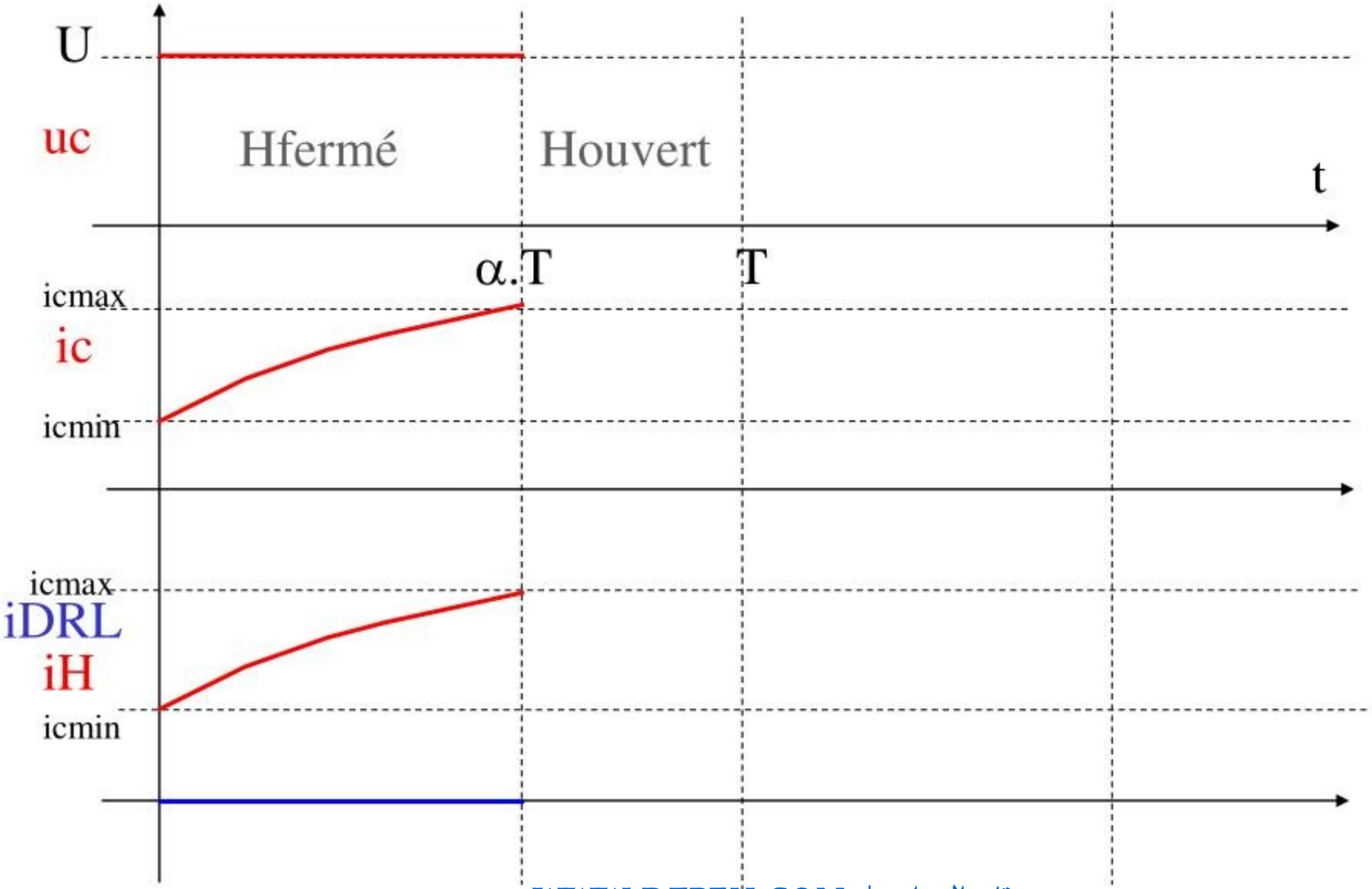
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1) Courant ininterrompu (cas général)



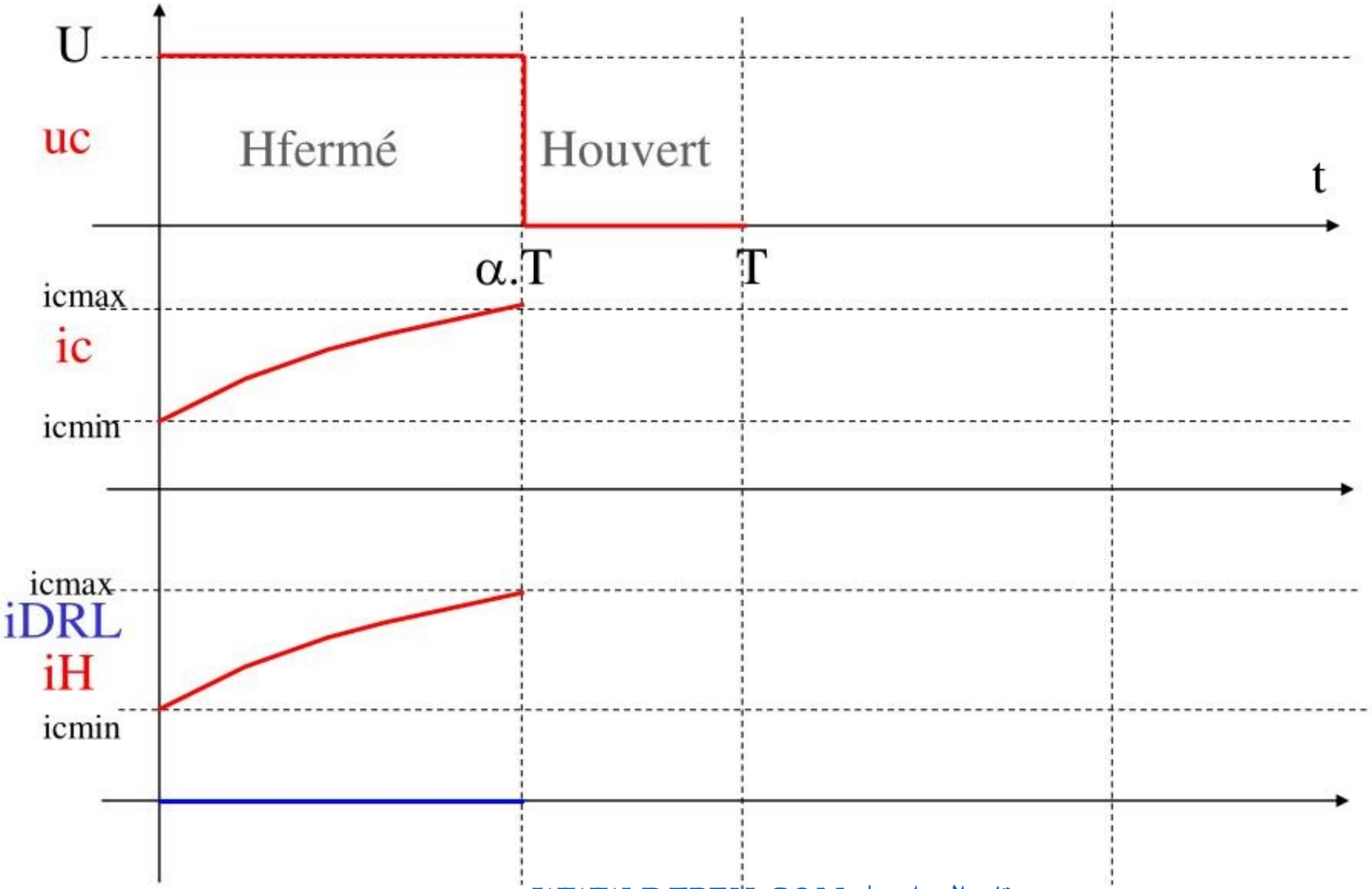
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1) Courant ininterrompu (cas général)



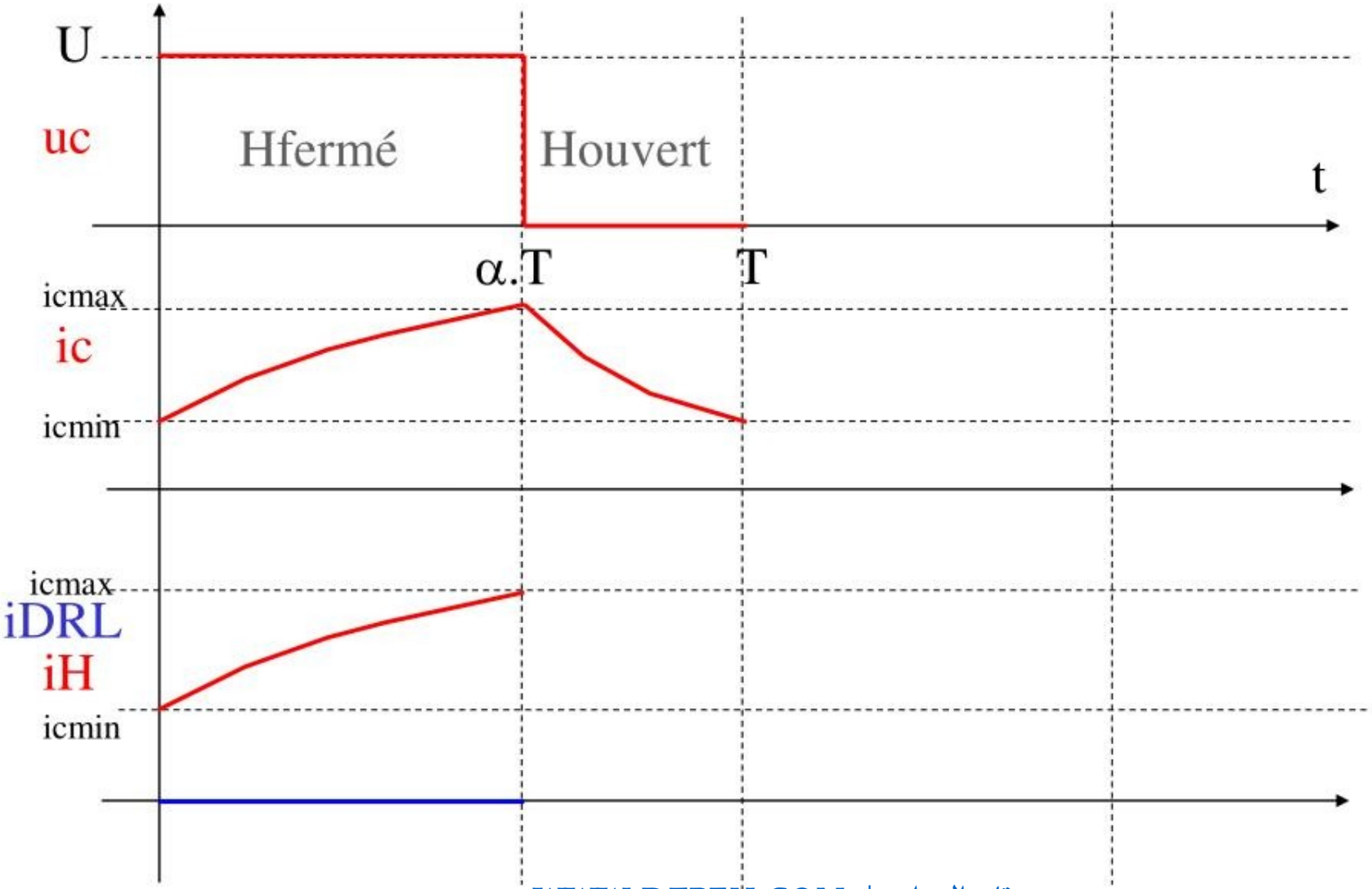
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1) Courant ininterrompu (cas général)



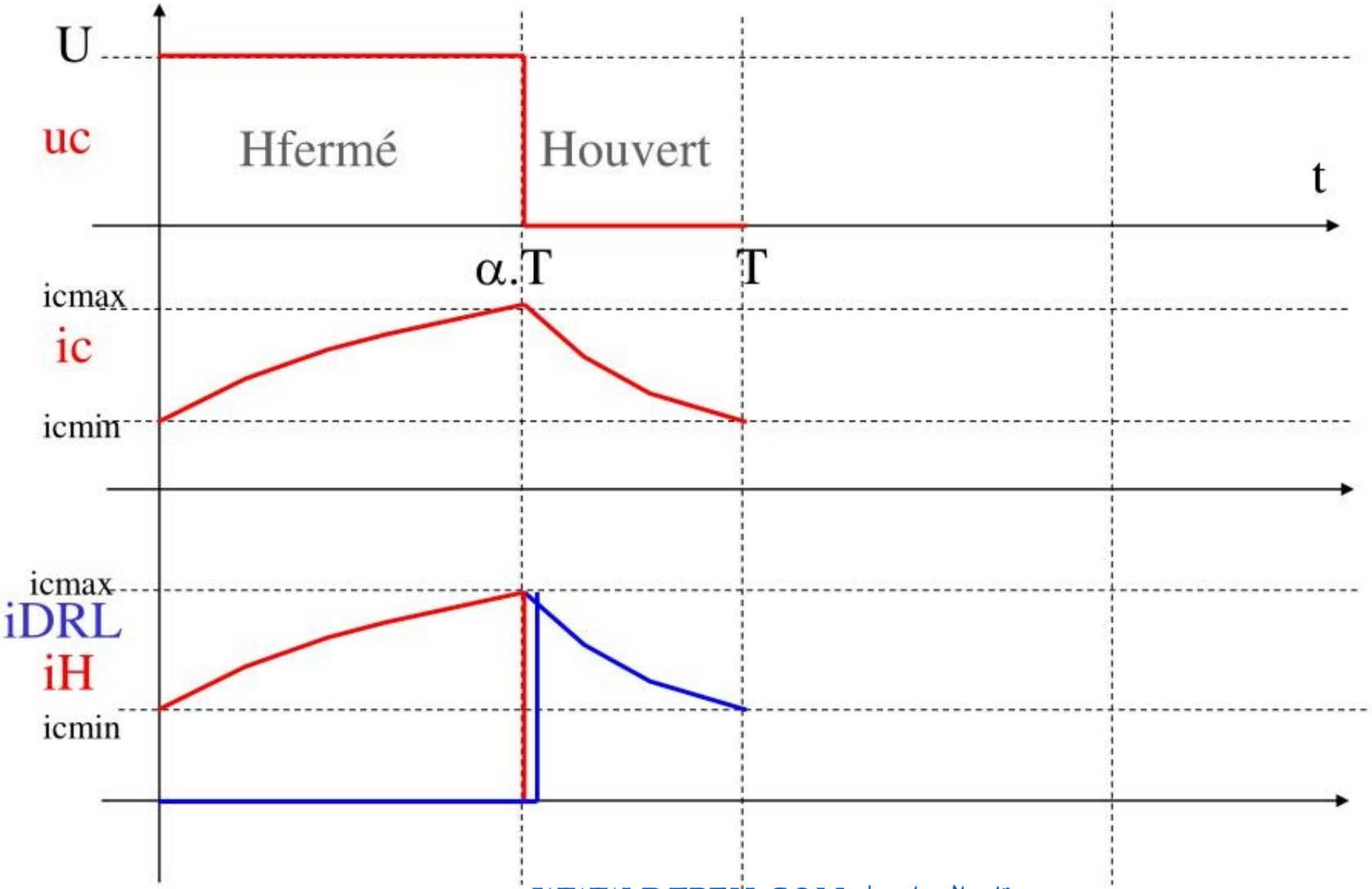
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1) Courant ininterrompu (cas général)



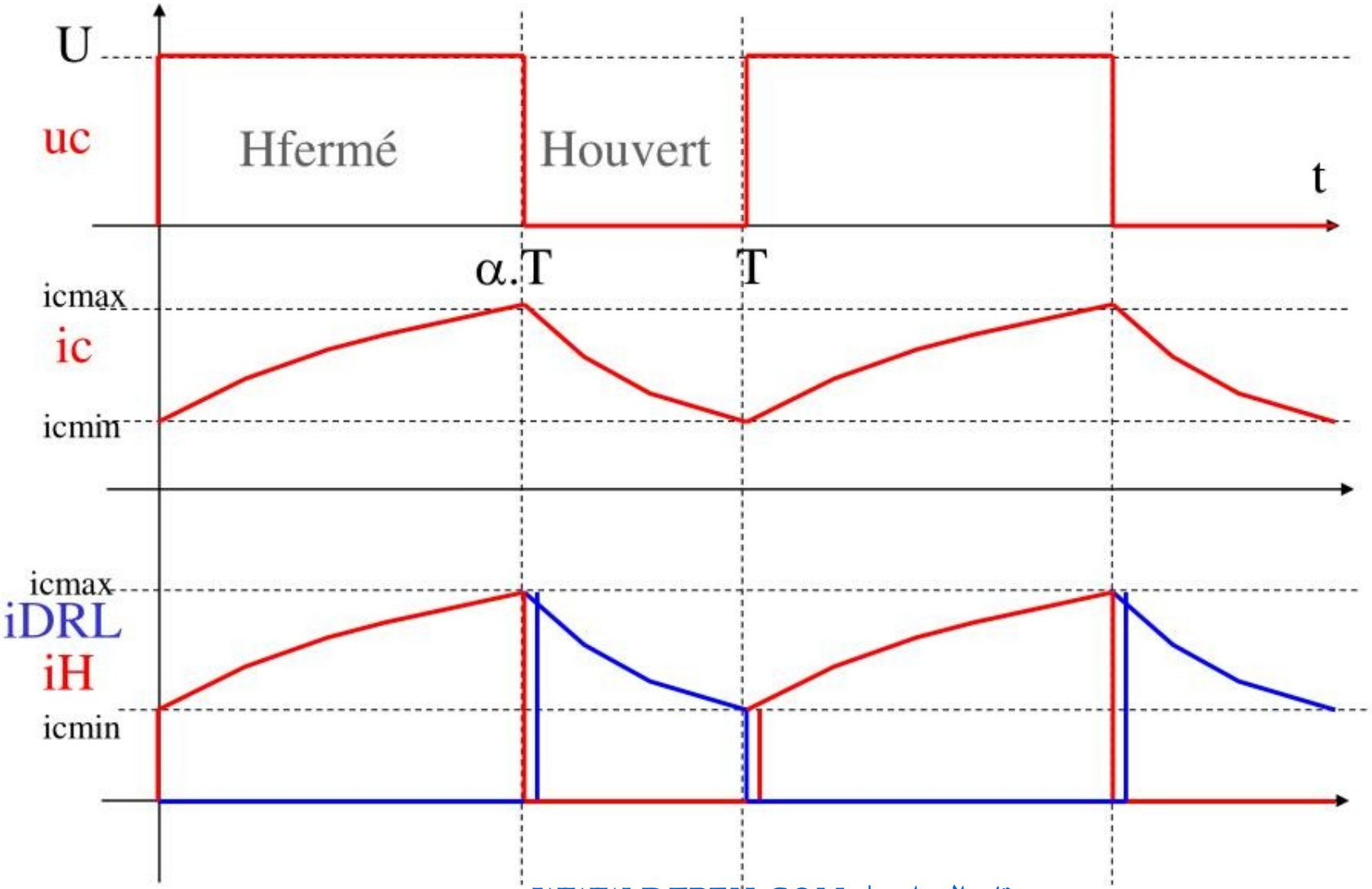
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1) Courant ininterrompu (cas général)



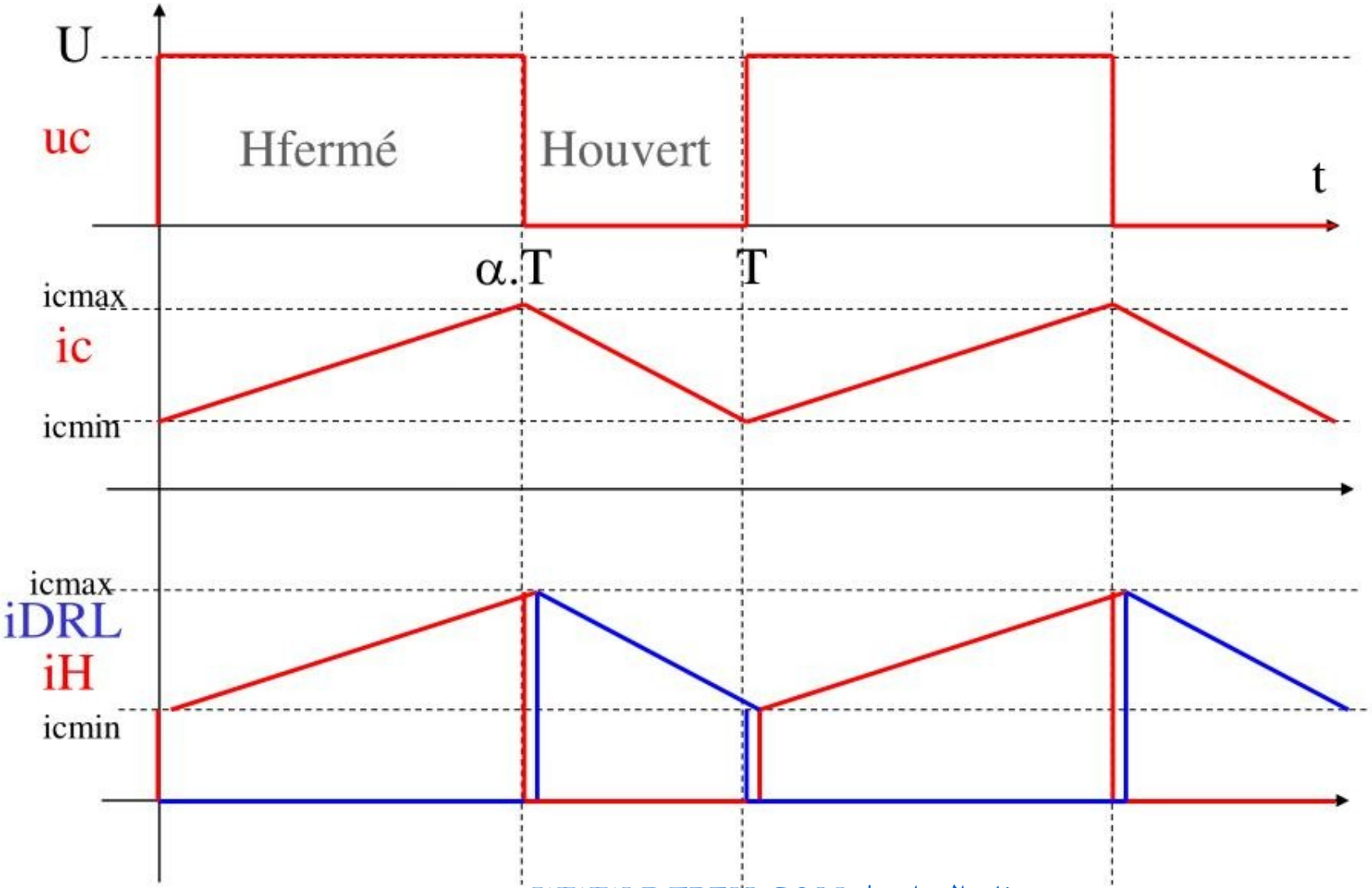
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1) Courant ininterrompu (cas général)



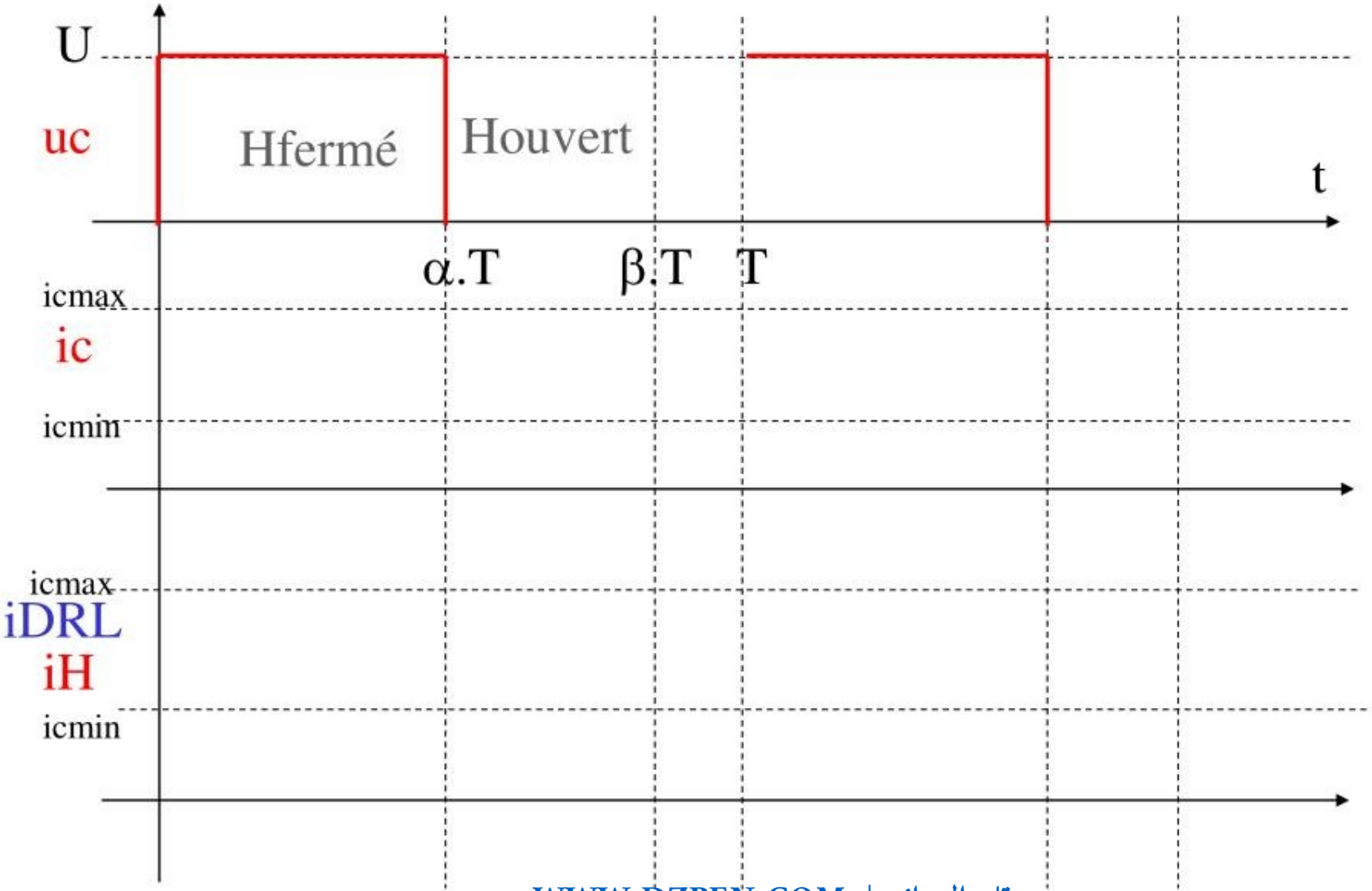
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REMARQUE: Courant ininterrompu (cas fréquent $\tau >> T$)



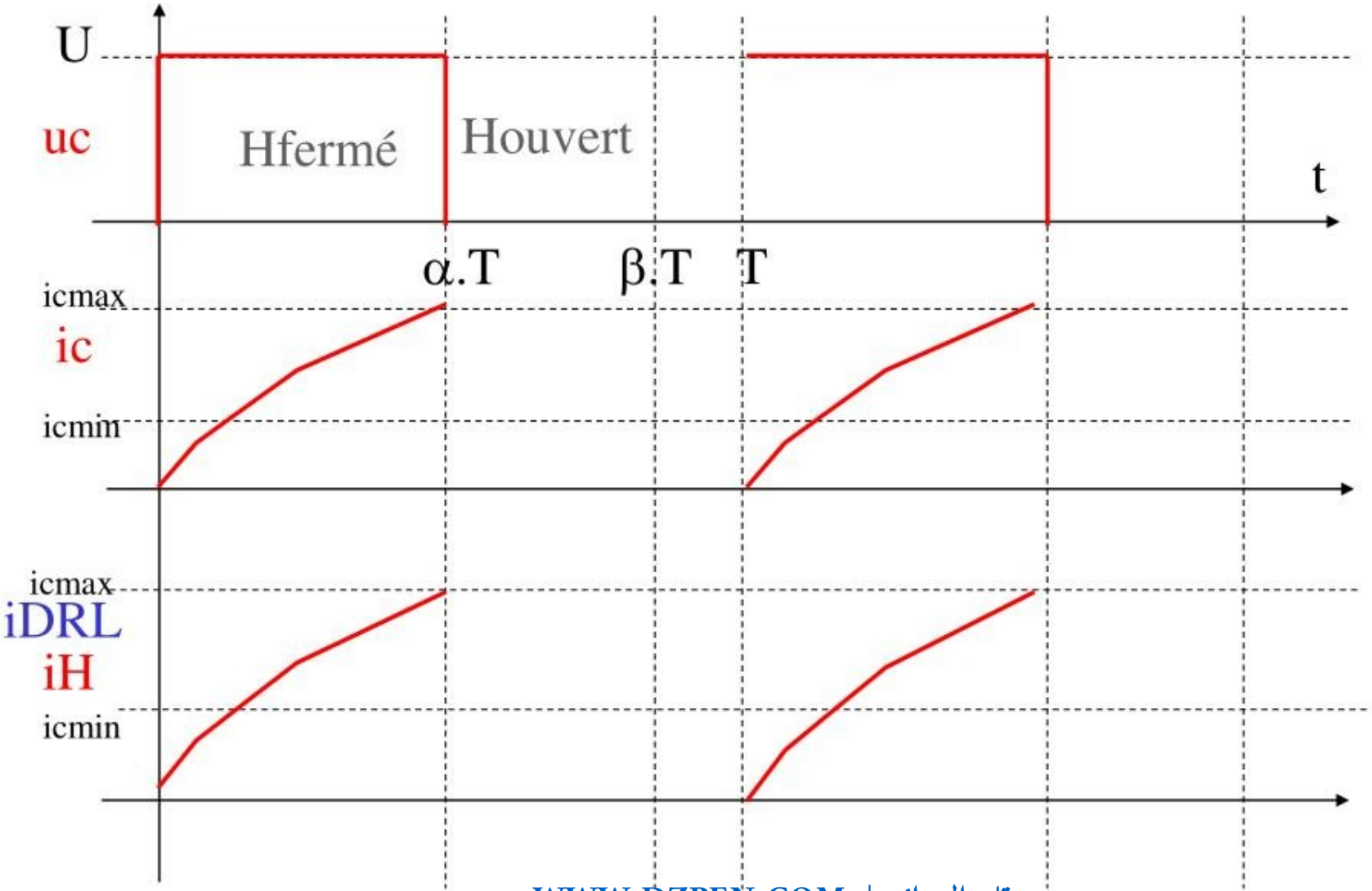
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2) Courant interrompu (cas général)



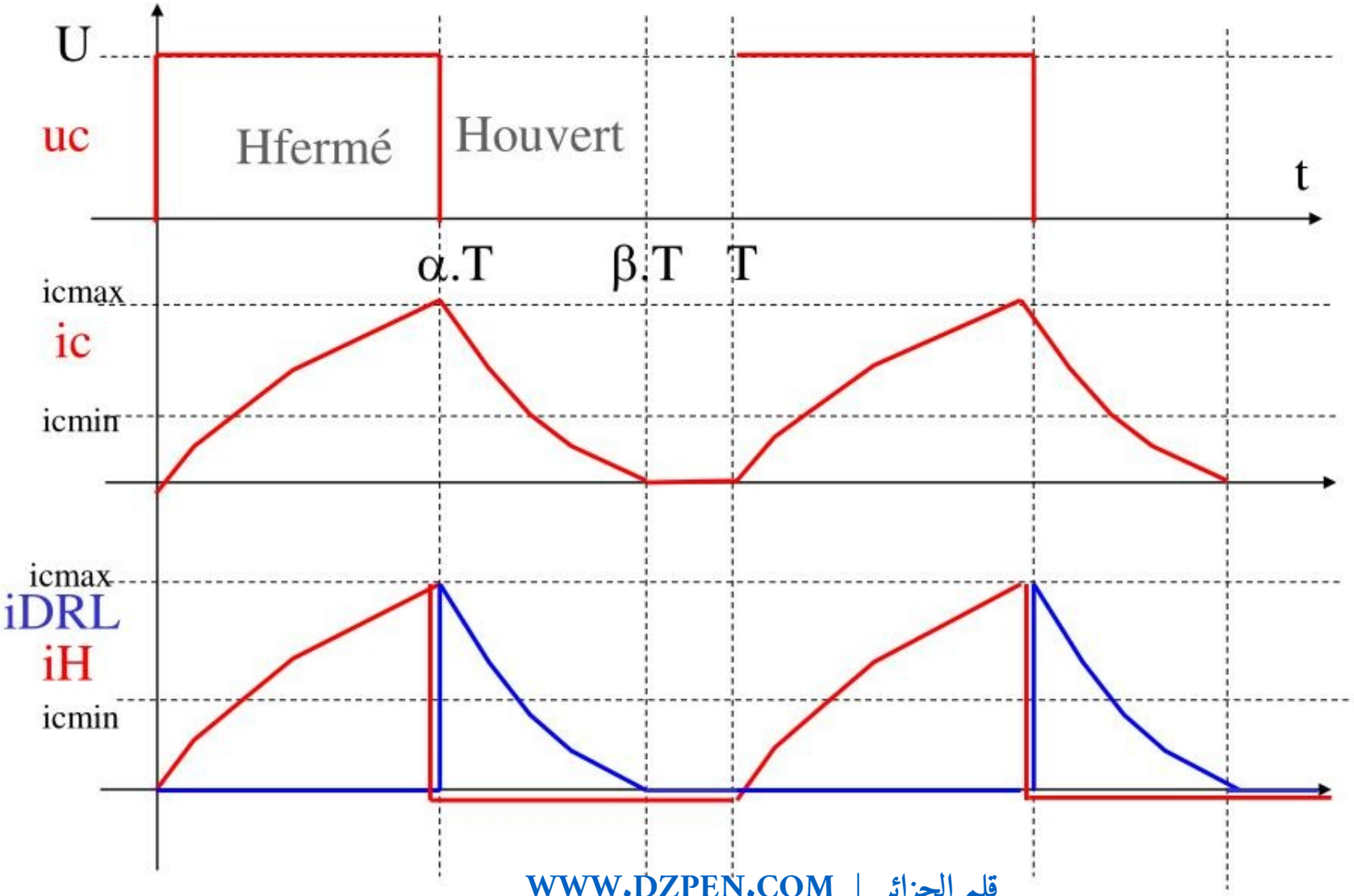
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2) Courant interrompu (cas général)



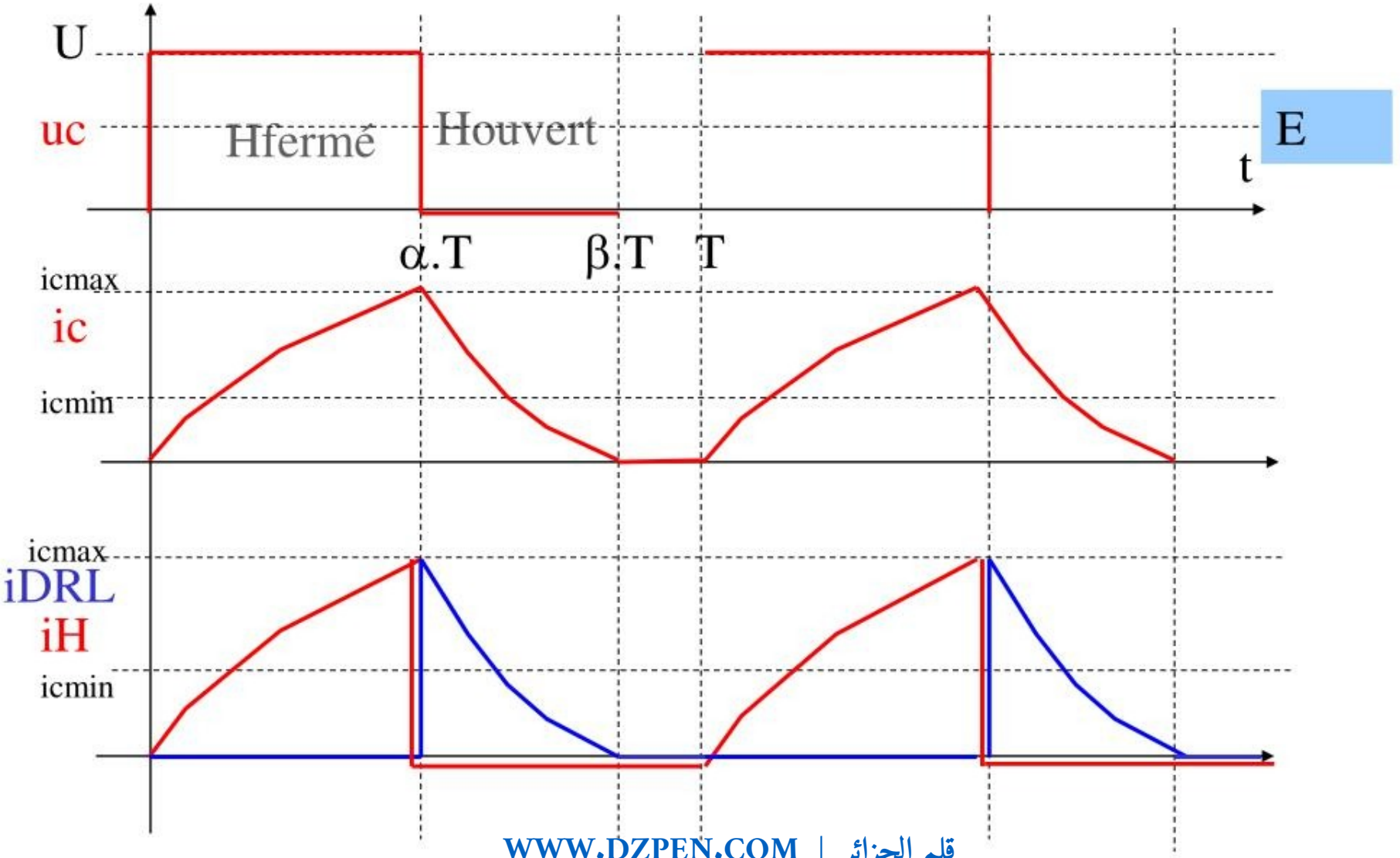
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2) Courant interrompu (cas général)



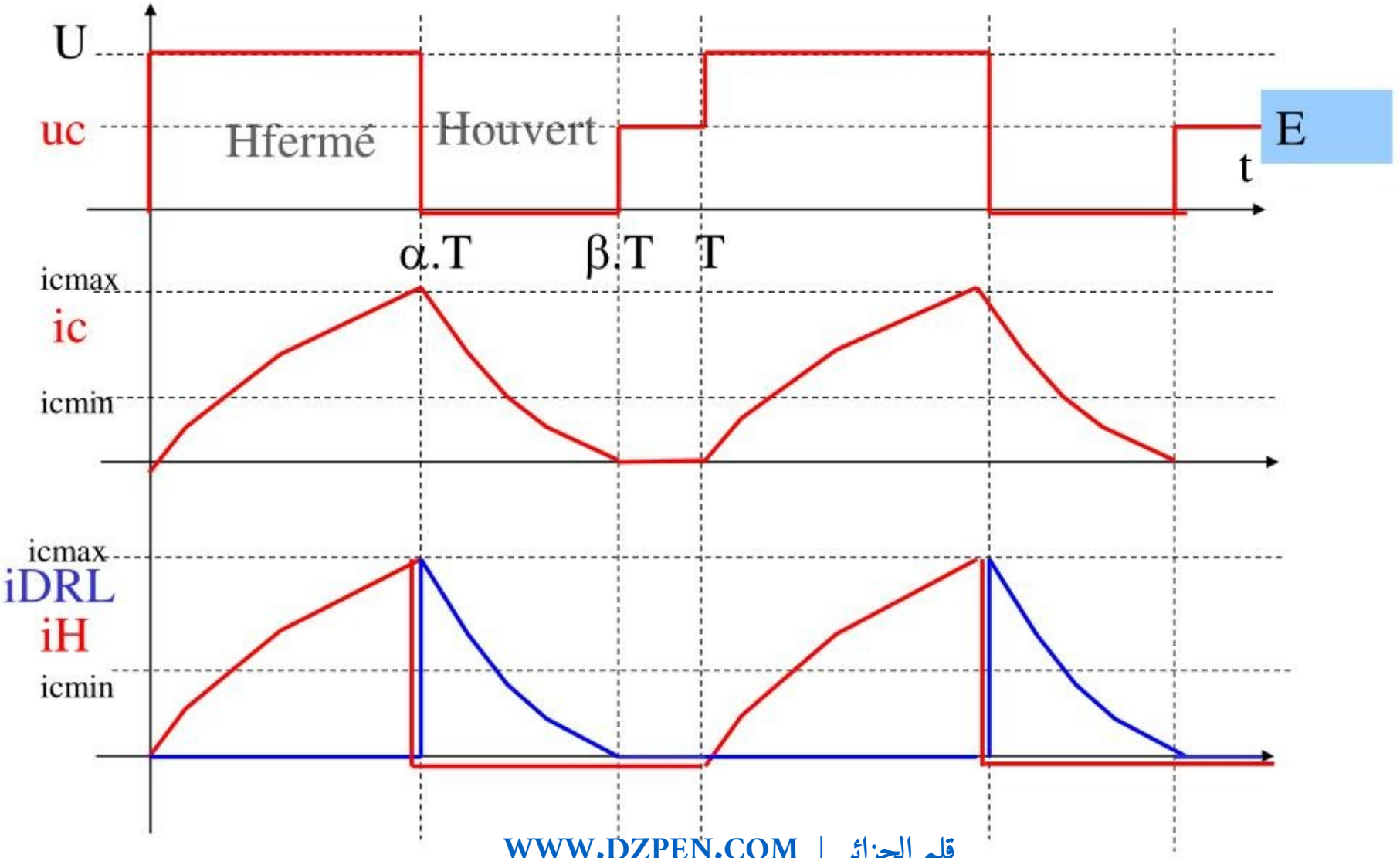
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2) Courant interrompu (cas général)



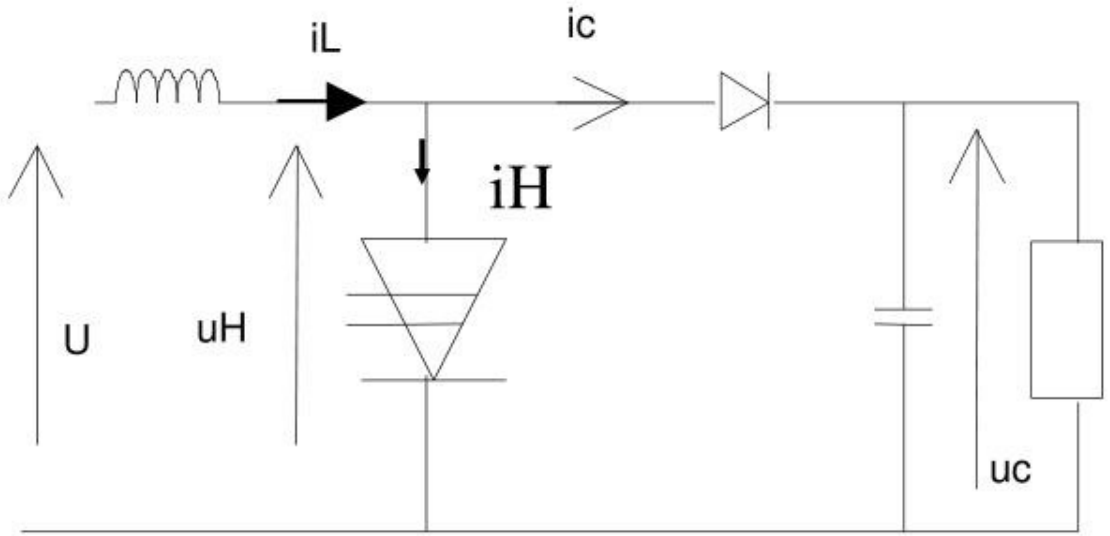
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2) Courant interrompu (cas général)



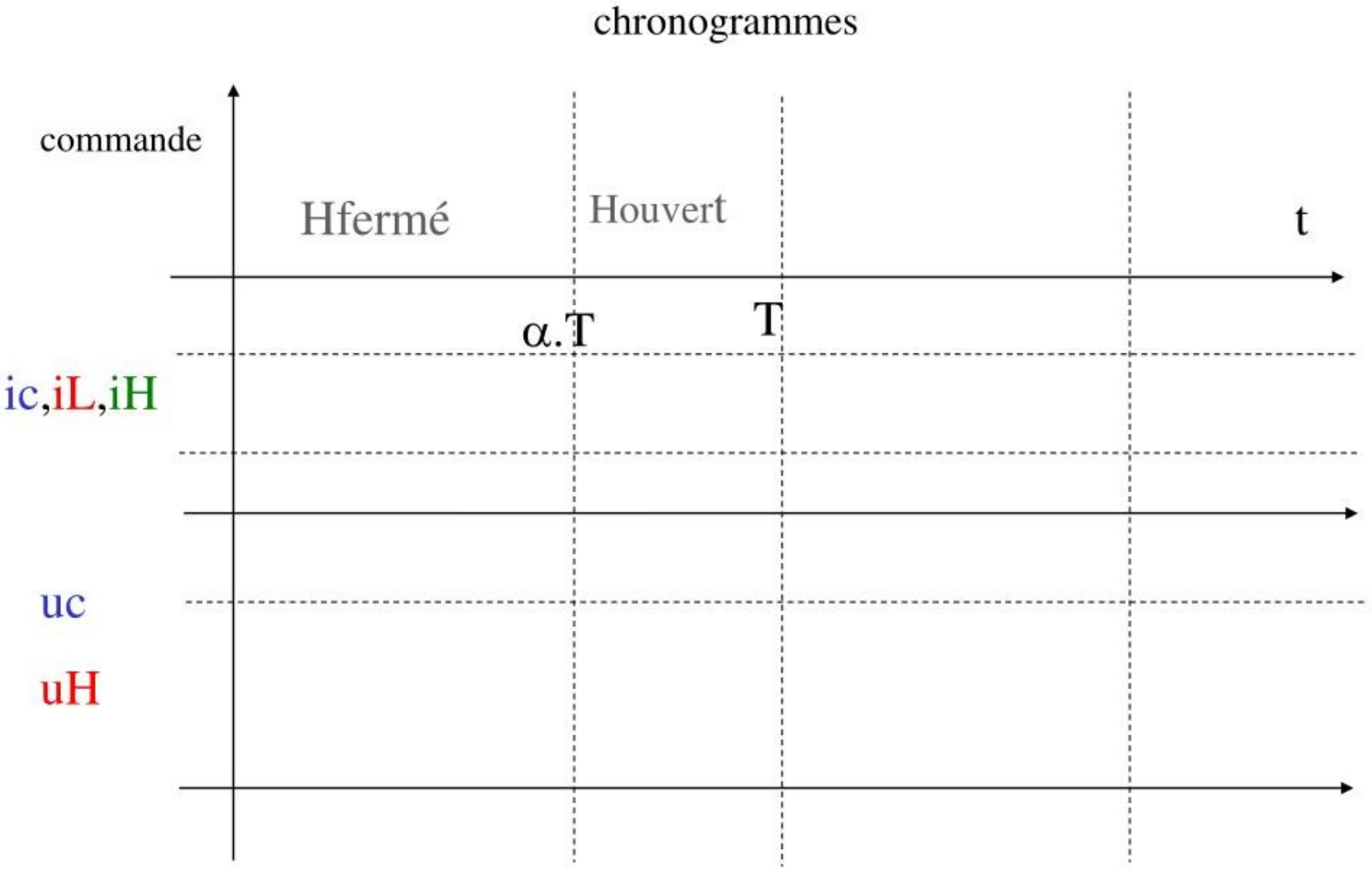
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III) Hacheur parallèle : élévateur

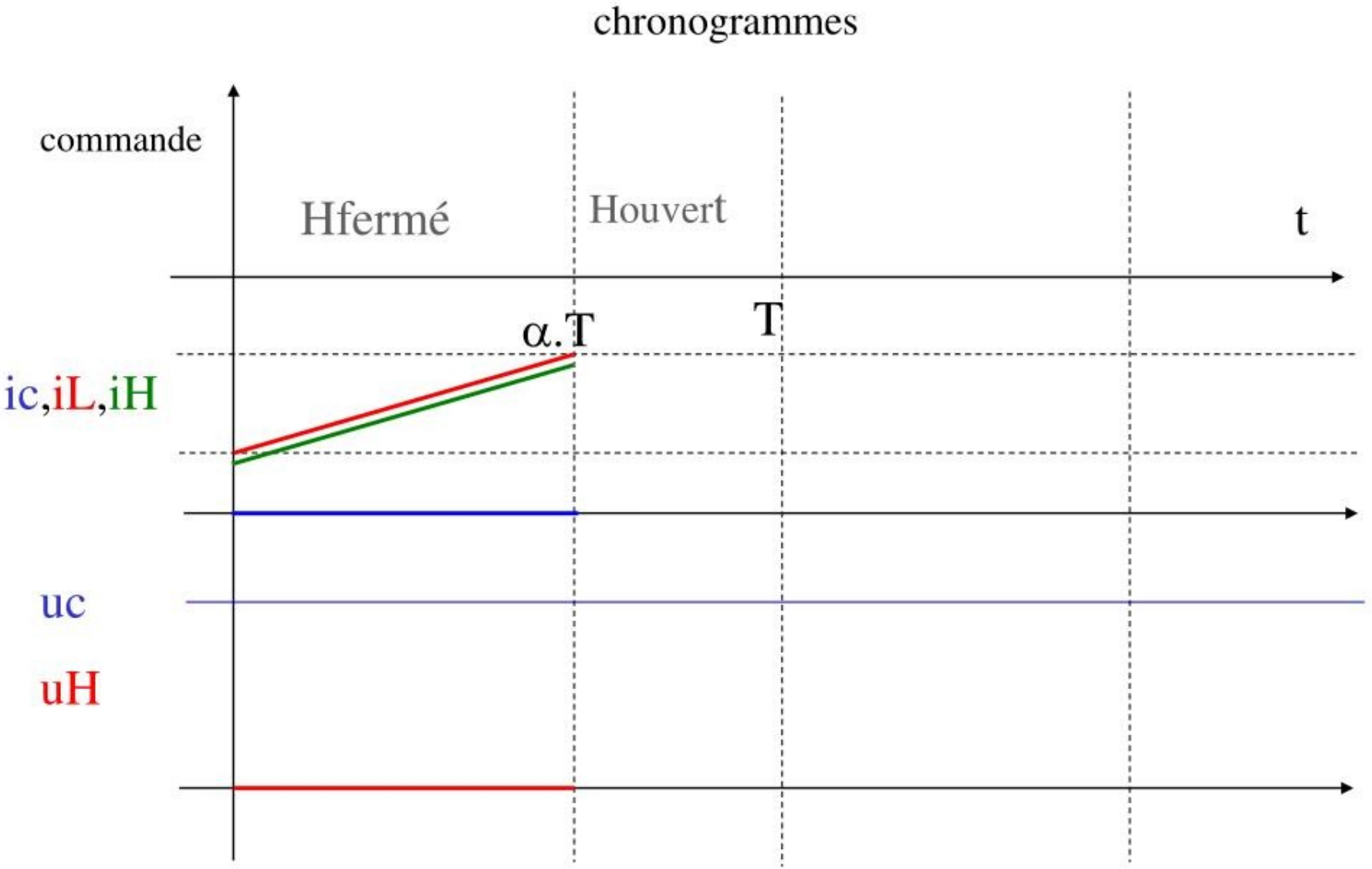


Hypothèse courant linéaire

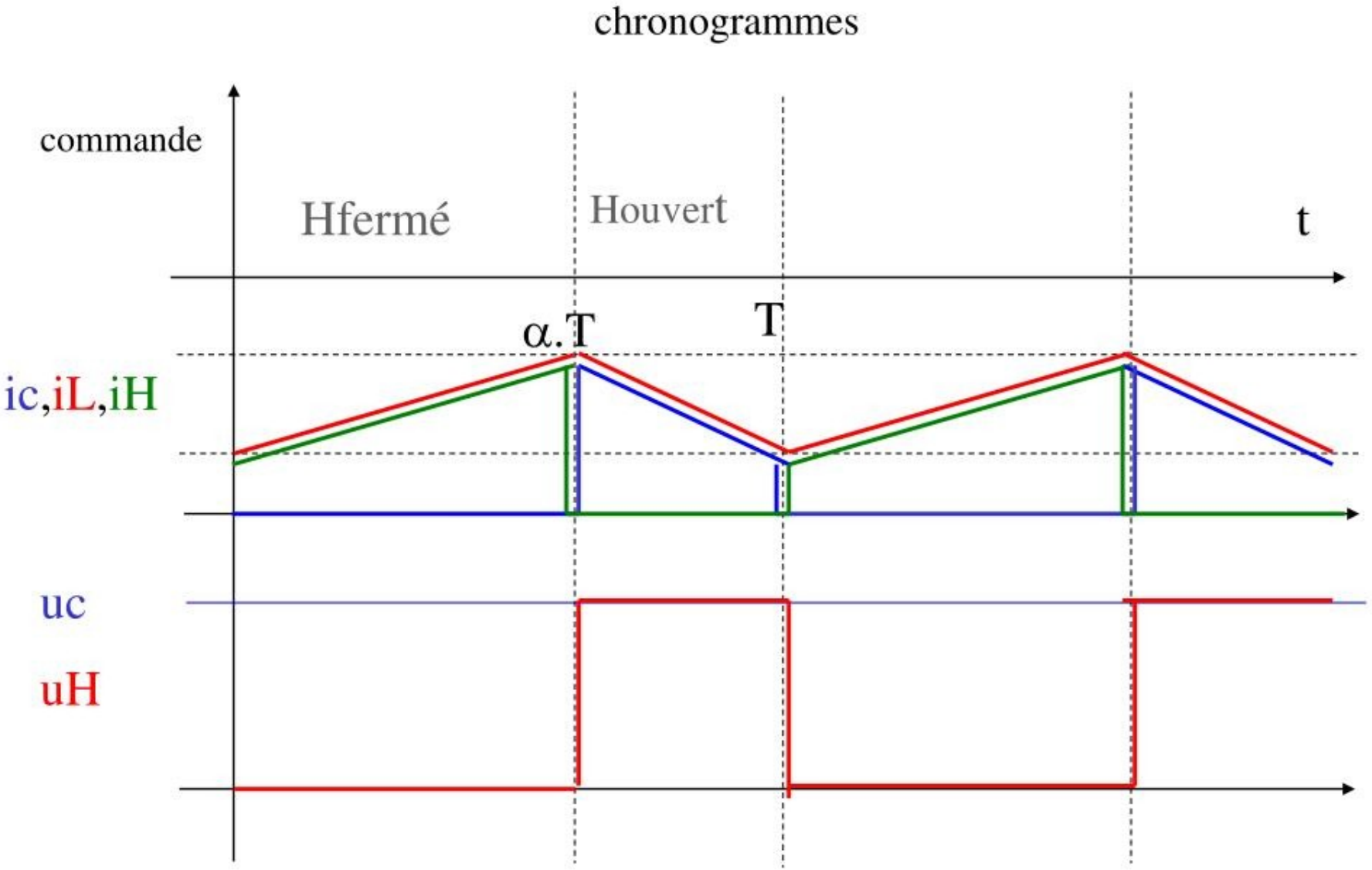
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